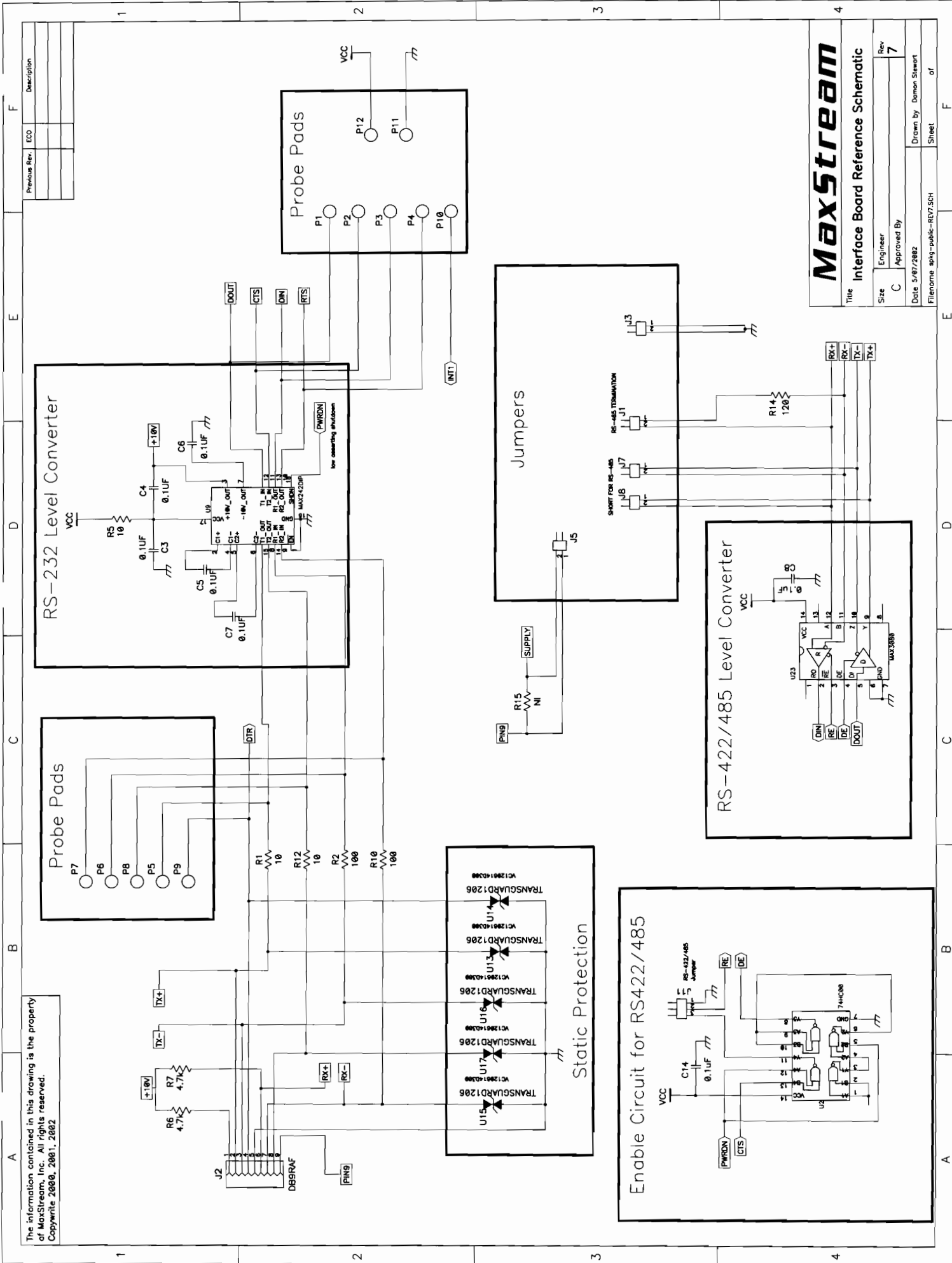


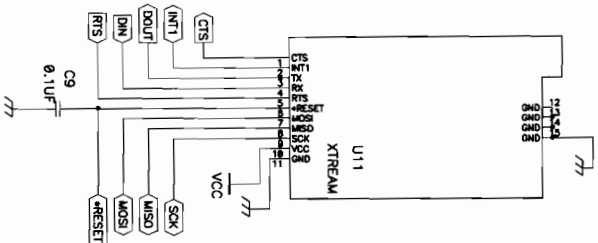
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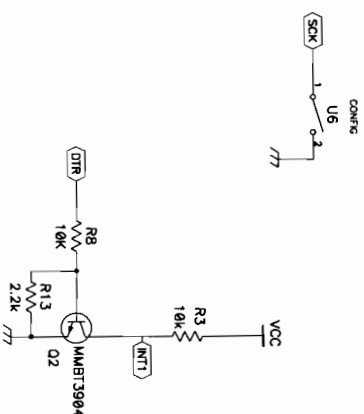
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Title: Interface Board Reference Schematic

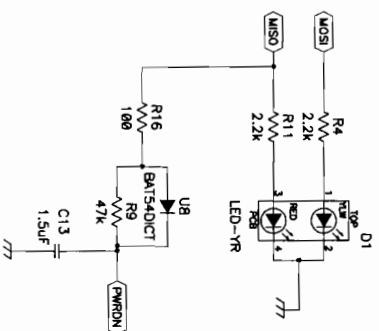
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Date	5/07/2002	Drawn by	Damon Stewart	7
Filename	apkg-public-REV7.SCH	Sheet		of

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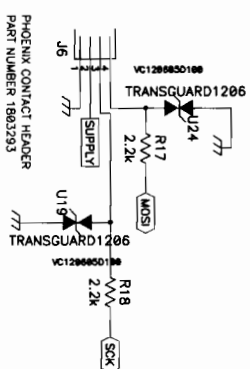
CONFIG Pin and SLEEP (INT1) Signal Circuits



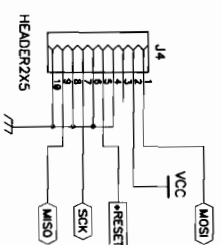
LEDs and Power Down circuit



I/O Screw Terminal



Programming Header



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Interface Board Reference Schematic

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Date 5/8/2002		Drawn by Damm
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Previous Rev.	ECO	Description